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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/790,911	03/02/2004	Robert G. Gels	Analog. 7099	1408	
55740	7590 07/06/2006		EXAMINER		
GAUTHIER & CONNORS, LLP 225 FRANKLIN STREET			LE, LANA N		
BOSTON, M			ART UNIT	PAPER NUMBER	
2001011, 1			2618	-	
			DATE MAILED: 07/06/2006	5	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Applic	ation No.	Applicant(s)		
Office Action Summary		10/790),911	GELS ET AL.		
		Exami	ner	Art Unit		
		Lana N	. Le	2618		
Period fo	The MAILING DATE of this communica r Reply	ntion appears on	the cover sheet	with the correspondence a	ddress	
A SHOWHIC - Exter after - If NO - Failu Any o	ORTENED STATUTORY PERIOD FOR HEVER IS LONGER, FROM THE MAI asions of time may be available under the provisions of SIX (6) MONTHS from the mailing date of this community period for reply is specified above, the maximum statute to reply within the set or extended period for reply will eply received by the Office later than three months after adjustment. See 37 CFR 1.704(b).	LING DATE OF 37 CFR 1.136(a). In no cation. ory period will apply an , by statute, cause the	THIS COMMUN event, however, may d will expire SIX (6) MO application to become	IICATION. a reply be timely filed ONTHS from the mailing date of this ABANDONED (35 U.S.C. § 133).		
Status						
2a)□	Responsive to communication(s) filed of This action is FINAL . 2b Since this application is in condition for closed in accordance with the practice	This action is	s non-final. opt for formal ma		ne merits is	
Dienociti	on of Claims			,		
4)⊠ 5)□ 6)⊠ 7)⊠	Claim(s) <u>1-21</u> is/are pending in the app 4a) Of the above claim(s) is/are Claim(s) is/are allowed. Claim(s) <u>1-11, and 15-21</u> is/are rejecte Claim(s) <u>12-14</u> is/are objected to. Claim(s) are subject to restriction	withdrawn from d.				
Applicati	on Papers					
10)	The specification is objected to by the E The drawing(s) filed on is/are: a Applicant may not request that any objection Replacement drawing sheet(s) including the The oath or declaration is objected to b) accepted or on to the drawing(see correction is req	s) be held in abeyout uired if the drawin	ance. See 37 CFR 1.85(a).	` '	
Priority u	nder 35 U.S.C. § 119					
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 						
2) Notice	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO		Paper No	/ Summary (PTO-413) o(s)/Mail Date		
	nation Disclosure Statement(s) (PTO-1449 or PT · No(s)/Mail Date	O/SB/08)	5) Notice of Other:	f Informal Patent Application (PT	ГО-152)	

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DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1, 3 and 7-10 are rejected under 35 U.S.C. 102(b) as being anticipated by Yukio et al (JP 06-177,680).

Regarding claim 1, Yukio et al disclose a radio frequency output power control system (fig. 1) for use in communication systems that use a modulation scheme having a non-constant amplitude envelope (envelope detecting via 41, 411 which changes according to signal S13; paras. 15, 17-18), said power control system comprising:

a power amplifier (15) having a power amplifier input (input of 15) for receiving an input signal with a non-constant amplitude envelope (para. 18), a power control input for receiving a power control signal (from control circuit 14), and a power amplifier output (output of 15) for providing an amplified output signal (para. 21):

a track and hold circuit (43) for tracking a measured reference power signal that is representative of a modulation of the input signal (para. 20); and

subtraction means (42) for subtracting an output of said track and hold circuit from said measured reference power signal to provide a difference signal that is coupled to the power control input (para. 21).

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Regarding claim 3, Yukio et al disclose the radio frequency output power control system as claimed in claim 1, wherein the track and hold circuit is responsive to a HOLD.sub.on signal (P43; para. 20).

Regarding claim 7, Yukio et al disclose the radio frequency output power control system as claimed in claim 1, wherein said track and hold circuit (43) is coupled to said power amplifier (15) via an error amplifier (33).

Regarding claim 8, Yukio et al disclose the radio frequency output power control system as claimed in claim 1, wherein said system is employed to control power for a modulated signal with non-constant envelope (para. 18; envelope and voltage changes according to transmitted signal S13).

Regarding claim 9, Yukio et al disclose the radio frequency output power control system as claimed in claim 1, wherein a feedback signal is subtracted from an output signal of said subtraction means (42).

Regarding claim 10, Yukio discloses the radio frequency output power control system as claimed in claim 1, wherein said system is further responsive to a TX.sub.Ramp signal (from 35, 14 to ramp the power level of the PA) (para. 6).

3. Claims 15, and 20-21 are rejected under 35 U.S.C. 102(b) as being anticipated by Davidson (US 5,054,116).

Regarding claim 15, Davidson discloses a radio frequency output power control system comprising:

a power amplifier (308) coupled to an input signal (input signal from 352);

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a reference logarithmic unit (332) coupled to a reference signal (336) (logarithmic unit similar to log unit 132 described in fig. 1; col 5, lines 22-25; see fig. 3);

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a track and hold circuit (355) coupled to said reference signal (via 2, 384);

a feedback logarithmic unit (314) coupled to a feedback signal (feedback signal received via detector 312) (col 6, lines 40-47); and combiner means (382) for providing a corrective signal (via 324, 306, 352) to said power amplifier (308) responsive to said reference signal (336, 380), an output signal (output signal from 355 via 321) from said track and hold circuit (355), and said feedback signal (feedback via 312, 316, 355) (col 6, lines 14-24, lines 40-61).

Regarding claim 20, Davidson discloses the radio frequency output power control system as claimed in claim 15, wherein Davidson does not disclose said combiner means (382) is further responsive to a TX.sub.Ramp signal (340 to 350, 352 to ramp power of 308) (fig. 3).

Regarding claim 21, Davidson discloses the radio frequency output power control system as claimed in claim 15, wherein said track and hold circuit is responsive to a HOLD.sub.on signal (col 6, lines 47-54).

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

4. Claim 2 is rejected under 35 U.S.C. 103(a) as being unpatentable over Yukio et al (JP 06-177,680) in view of Pehlke et al (US 6,566,944).

Regarding claim 2, Yukio et al disclose the radio frequency output power control system as claimed in claim 1, wherein Yukio et al do not disclose said track and hold circuit and said measured reference power signal are coupled to the input signal via a logarithmic power detect unit. Pehlke et al disclose a track and hold circuit (116) and said measured reference power signal (from 64) are coupled to the input signal (input of PA 48) via a power detect unit (detected at output of 102) (fig. 11). Yukio et al and Pehlke et al do not disclose a logarithmic detect unit. It would have been obvious to one of ordinary skill in the art at the time the invention was made to have logarithmic detect unit in order to convert the reference signal and switching to a DC level representing the power of the reference input signal.

5. Claim 4 is rejected under 35 U.S.C. 103(a) as being unpatentable over Yukio et al (JP 06-177,680) in view of Nitta (US 6,617,930).

Regarding claim 4, Yukio et al disclose the radio frequency output power control system as claimed in claim 1, wherein Yukio et al do not disclose the input signal is an IF output signal provided by a transmitter unit. Nitta discloses an input signal (to be inputted to PA 5 via 6, 7) is an IF output signal (IF output signal from an indoor unit transmitter) provided by a transmitter unit (indoor unit transmitter) (col 1, lines 55-62; col 2, lines 10-23). It would have been obvious to one of ordinary skill in the art at the time

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the invention was made to have the input signal be an IF signal in order to provide only the necessary DC voltage from an indoor transmitter to the power amplifier to reduce power usage as suggested by Nitta (col 3, lines 20-39).

6. Claim 5 is rejected under 35 U.S.C. 103(a) as being unpatentable over Yukio et al (JP 06-177,680) in view of Vakilian et al (US 6,795,712).

Regarding claim 5, Yukio et al disclose the radio frequency output power control system as claimed in claim 1, wherein said input signal comprises baseband reference outputs from a transmitter unit. Vakilian the input signal comprises baseband reference outputs from a transmitter unit (col 5, lines 10-18). It would have been obvious to one of ordinary skill in the art at the time the invention was made to have the input signal to the power amplifier be transmitted from a baseband reference output in order to provide a reference voltage to the power control circuit to adjust the power of the RF amplifier.

7. Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over Yukio et al (JP 06-177,680) in view of Komatsu (US 6,144,860).

Regarding claim 6, Yukio et al disclose the radio frequency output power control system as claimed in claim 5, wherein Yukio et al do not disclose said system further includes a pair of squaring units. Komatsu discloses a transmission power control system including a pair of squaring units (44, 45) (col 9, lines 25-31). It would have been obvious to one of ordinary skill in the art at the time the invention was made to add the squaring units to Yukio et al in order to calculate the desired power level to compare with the reference level.

8. Claim 11 is rejected under 35 U.S.C. 103(a) as being unpatentable over Yukio et

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al (JP 06-177,680) in view of McKay Sr. et al (US 2004/0,166,802).

Regarding claim 11, Yukio et al discloses the radio frequency output power control system as claimed in claim 1, wherein Yukio et al do not disclose said system further includes a feedback logarithmic power detect unit. McKay Sr. et al system further includes a feedback logarithmic power detect unit (para. 109; fig. 19). It would have been obvious to one of ordinary skill in the art at the time the invention was made to have a log power detector in order to detect power with commensurate accuracy which enables the implementation of signal amplitude control that can function without an individual calibration for each exemplary unit as suggested by McKay Sr. et al.

9. Claim 16 is rejected under 35 U.S.C. 103(a) as being unpatentable over Davidson (US 5,054,116) in view of Humpherys (US 5,656,929).

Regarding claim 16, Davidson discloses the radio frequency output power control system as claimed in claim 15, wherein Davidson does not disclose said reference signal includes a modulated RF signal. Humpherys discloses an RF modulated reference signal (col 1, lines 27-49). It would have been obvious to one of ordinary skill in the art at the time the invention was made to have the reference voltage come from an RF source in order to compare the voltage from a reference RF signal with the output RF detected signal within the same frequency band.

10. Claim 17 is rejected under 35 U.S.C. 103(a) as being unpatentable over Davidson (US 5,054,116) in view of Nitta (2003/0,092,407).

Regarding claim 17, Davidson discloses the radio frequency output power control system as claimed in claim 15, wherein Davidson does not disclose said reference

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signal includes a modulated IF signal. Yamada discloses converting a modulated IF signal into a reference voltage signal (paras. 64, 74). It would have been obvious to one of ordinary skill in the art at the time the invention was made to have an IF reference source and an IF detector generating the reference voltage of Davidson in order to compare an intermediate reference voltage with the output detected signal to compare the detected power level with a reference intermediate level to attenuate the power of the power amplifier.

11. Claim 18 is rejected under 35 U.S.C. 103(a) as being unpatentable over Davidson (US 5,054,116) in view of Vakilian et al (US 6,795,712).

Regarding claim 18, Davidson discloses the radio frequency output power control system as claimed in claim 15, wherein Davidson does not disclose said reference signal includes baseband I and Q signals. Vakilian discloses the reference signal includes baseband signals (col 5, lines 10-18). It would have been obvious to one of ordinary skill in the art at the time the invention was made to have the reference signal be baseband signals in order to provide a reference voltage to the power control circuit to adjust the power of the RF amplifier. Davidson and Vakilian do not disclose the reference baseband signal is I and Q signals. However, it is well known and notoriously old in the art to have the baseband signal be digital. It would have been obvious to one of ordinary skill in the art at the time the invention was made to have the baseband signal be I and Q signals in order to output differential signals in a digital system.

12. Claim 19 is rejected under 35 U.S.C. 103(a) as being unpatentable over Davidson (US 5,054,116) in view of Yukio et al (JP 06-177,680).

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Regarding claim 19, Davidson discloses the radio frequency output power control system as claimed in claim 18, wherein Davidson does not disclose said system is employed to control power for a modulated signal with non-constant envelope. Yukio et al disclose controlling power for a modulated signal with non-constant envelope (para. 15, 18). It would have been obvious to one of ordinary skill in the art at the time the invention was made to detect a non constant envelope in order to detect a changing envelope depending on the output signal level of the power amplifier.

Allowable Subject Matter

- 13. Claims 12-14 are allowable over the cited prior art.
- 14. The following is an examiner's statement of reasons for allowance:

Regarding claim 12, Davidson (US 5,054,116) discloses a radio frequency output power control system for use in communication systems that use a modulation scheme having a non-constant amplitude envelope (during pulse amplitude modulation mode, envelope of modulated RF signal at output 311 varies and fedback to control the RF output) (col 5, lines 40-61), said power control system comprising:

a power amplifier (308) having an input to receive an input signal with a non-constant amplitude envelope, a power control input (input of 308) for receiving a power control signal (pulse modulated control signal from 340, 352), and an output (output of 308) for providing an amplified output signal (311);

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a track and hold circuit (355) for tracking a reference signal (col 5, line 52-68); a first combiner (382) for providing a difference between said reference signal (336, 380) and an output signal of said track and hold circuit (355) (col 6, lines 14-21), However, Davidson and the cited prior art fail to disclose a second combiner for providing a difference between a feedback signal and an output of said first combiner wherein said power control signal is responsive to the difference between the feedback signal and the output of the first combiner.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

15. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Lana N. Le whose telephone number is (571) 272-7891. The examiner can normally be reached on M-F 9:30-18:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Edward F. Urban can be reached on (571) 272-7899. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Lana Le

LANA LE PRIMARY EXAMINER